中山大學 SUN YAT-SEN UNIVERSITY
计算机学院（软件学院）
SCHOOL OF COMPUTER SCIENCE AND ENGINEERING

Compilation Principle
编 译 原 理

第20讲：中间代码(3)

张献伟

xianweiz.github.io

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Quiz

Handwritten, or email to
chenhq79@mail2.sysu.edu.cn

- Q1: Input and output of intermediate code generation?

  Input: AST + symbol table; output: IR

- Q2: TAC of a*b + c + d.

  $t_1 = a * b; t_2 = t_1 + c ; t_3 = t_2 + d;$

- Q3: What is SSA? What are the benefits?

  Single static assignment.

  Make dataflow explicit, facilitating IR optimizations.

- Q4: is the code SSA? If not, convert it.

  No. x is assigned more than once.

  $x_1 = a * b;$ if $x_1 > 0$: $x_2 = c;$ $y = PHI(x_1, x_2) + 2;$

```
x = a * b;
if x > 0: x = c;
y = x + 2;
```

- Q5: Explain LLVM Phi: %5 = phi i32 [%7, %4], [%1, %2].

  Value of %5 either from block %4 (reg: %7) or %2 (reg: %1).

Example: Branching

- Create branch
  - Create new basic block
  - Create conditional branch
  - Change IRBuilder's insert point

Global variable
Variable assignment
Binary operation
Branch

```
// Assume we already have created the "aGreaterThanZero" instruction
auto ifBB = llvm::BasicBlock::Create(TheContext, "", function);
auto elseBB = llvm::BasicBlock::Create(TheContext, "", function);

// br i1 %2, label %3, label %4
builder.CreateCondBr(aGreaterThanZero, ifBB, elseBB);

// Insert in the "if" basic block
// 3:                ; preds = %entry
//   ret i32 1
builder.SetInsertPoint(ifBB);
builder.CreateRet(builder.getInt32(1));

// Insert in the "else" basic block
// 4:                ; preds = %entry
//   ret i32 0
builder.SetInsertPoint(elseBB);
builder.CreateRet(builder.getInt32(0));
```

Code Generation[代码生成]

- Translations
  - Variable definitions[变量定义]
  - Assignment[赋值]
  - Array references[数组引用]
  - Boolean expressions[布尔表达式]
  - Control-flow statements[控制流语句]
- To generate three-address codes (TACs)
  - Lay out variables in memory
  - Generate TAC for any subexpressions or substatements
  - Using the result, generate TAC for the overall expression
- We can also use the syntax-directed formalisms to specify translations

SDT Translation of Booleans[布尔表达式]

- $B \rightarrow B_1 \,||\, B_2$
  - $B_1.true$ is same as $B.true$, B_2 must be evaluated if B_1 is false[B_1假才评估B_2]
  - The true and false exits of B_2 are the same as B[B_2与B同真假]
- $B \rightarrow E_1\ relop\ E_2$
  - Translated directly into a comparison TAC inst with jumps

B_1为真，跳转到B.true　　B_1为假，跳转到别处（需要继续评估B_2）

① $B \rightarrow \{ B_1.true = B.true;\ B_1.false = newlabel();\ \} B_1$
　　$|| \{ label(B_1.false);\ B_2.true = B.true;\ B_2.false = B.false;\ \} B_2$

② $B \rightarrow \{ B_1.true = newlabel();\ B_1.false = B.false;\ \} B_1$
　　$\&\& \{ label(B_1.true);\ B_2.true = B.true;\ B_2.false = B.false;\ \} B_2$

③ $B \rightarrow E_1\ relop\ E_2 \{ gen(\text{‘if’}\ E_1.addr\ relop\ E_2.addr\ \text{‘goto’}\ B.true);$
　　$gen(\text{‘goto’}\ B.false);\ \}$

④ $B \rightarrow !\ \{ B_1.true = B.false;\ B_1.false = B.true;\ \} B_1$

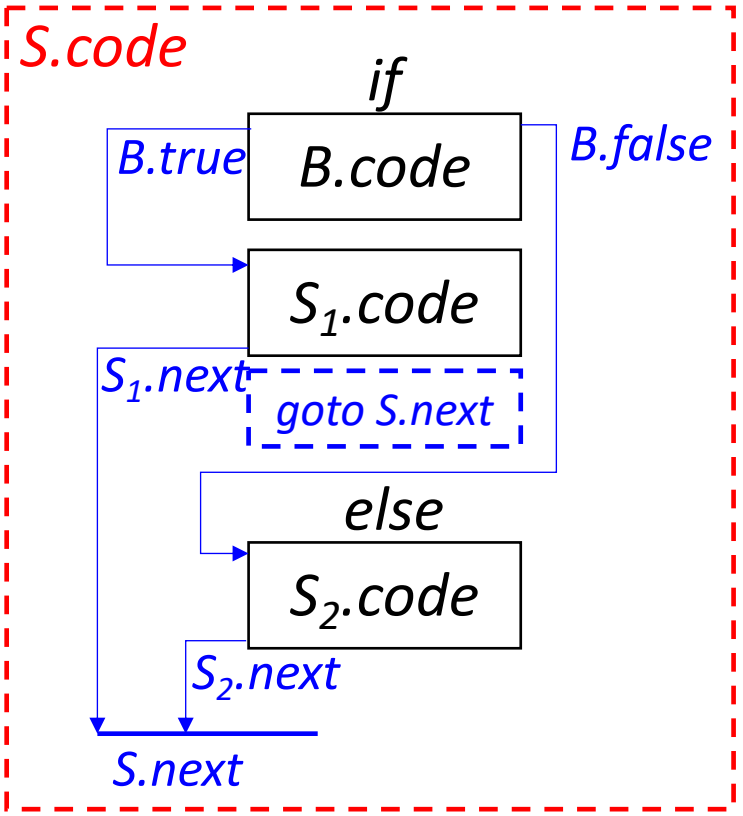
⑤ $B \rightarrow$ true $\{ gen(\text{‘goto’}\ B.true);\ \}$

⑥ $B \rightarrow$ false $\{ gen(\text{‘goto’}\ B.false);\ \}$

B: a boolean expression
S: a statement

CodeGen: Control Statement[控制语句]

① S -> if (B) S_1
② S -> if (B) S_1 else S_2
③ S -> while (B) S_1

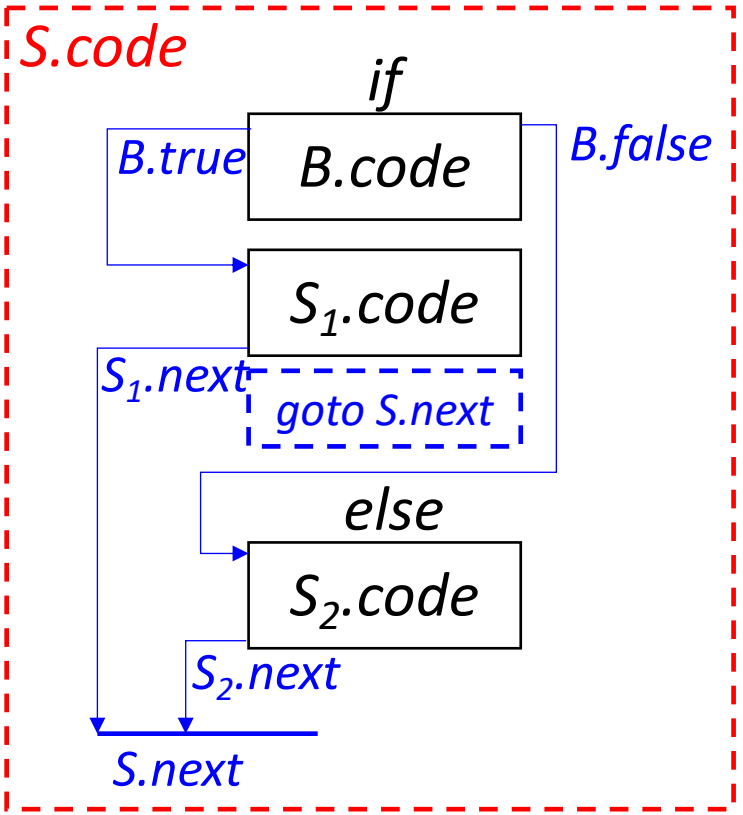


- Inherited attributes[继承属性]
  - *B.true*: the label to which control flows if *B* is true(依赖于S_1)
  - *B.false*: the label to which control flows if *B* is false(依赖于S_2)
  - *S.next*: a label for the instruction immediately after the code of *S*

Translation of Controls

① S -> if (B) S_1
② S -> if (B) S_1 else S_2
③ S -> while (B) S_1

S -> if { B.true = newlabel();
B.false = newlabel(); }
(*B*) *{ label(B.true); S_1.next = S.next; }*
S_1 { gen('goto' S.next); }
else *{ label(B.false); S_2.next = S.next; }* S_2



- Helper functions[辅助函数]
  - *newlabel()*: creates a new label
  - *label(L)*: attaches label *L* to the next three-address inst to be generated

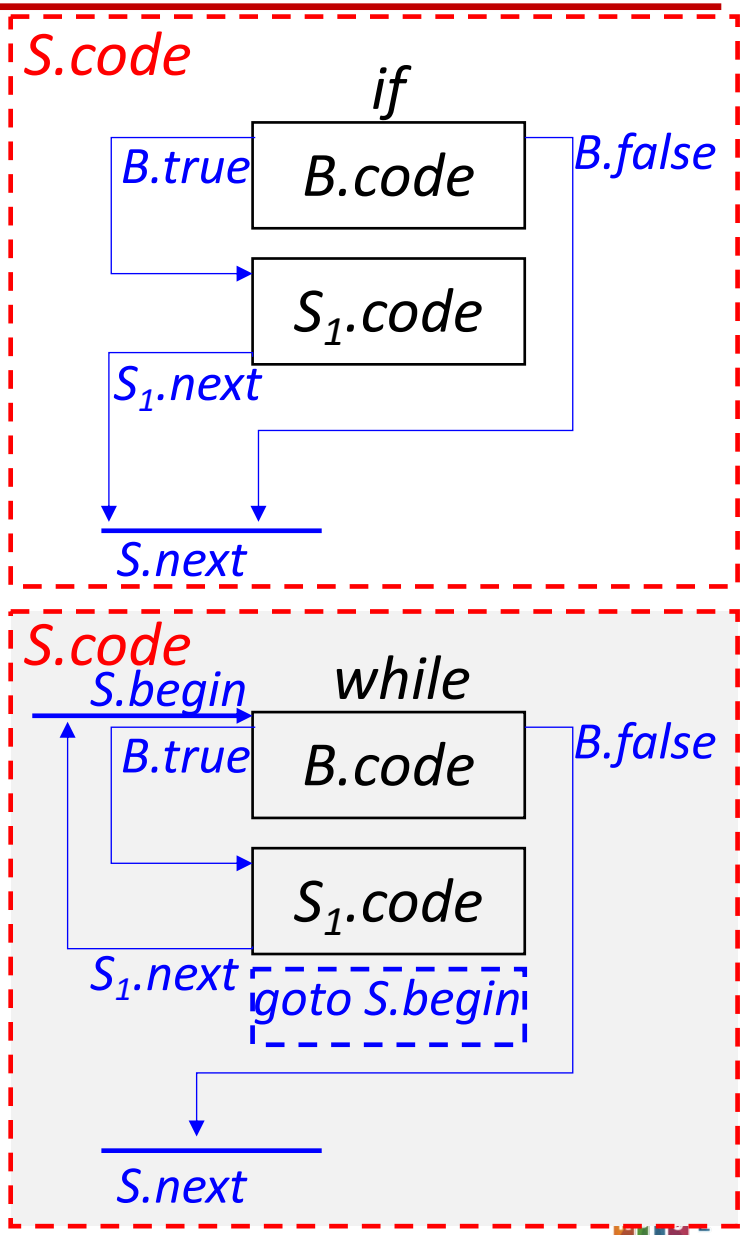
IfFalse *B* goto *B.false*
B.true:
S_1.code
goto *S.next*
B.false:
S_2.code
S.next:

Translation of Controls (cont.)

① S -> if (B) S_1
② S -> if (B) S_1 else S_2
③ S -> while (B) S_1

S -> if { B.true = newlabel();
B.false = S.next; }
(B) { label(B.true); S_1.next = S.next; }
S_1

S -> while { S.begin = newlabel();
label(S.begin);
B.true = newlabel();
B.false = S.next; }
(B) { label(B.true); S_1.next = S.begin; }
S_1 { gen('goto' S.begin); }



Jumping Labels[跳转标签]

- Key of generating code for Boolean and flow-control: matching a jump inst with the target of jump[跳转指令匹配到跳转目标]
  - Forward jump: a jump to an instruction below you
  - Label for jump target has not yet been generated
  - The labels are **not *L-attributed***[非左属性]

$B \rightarrow \{\ B_1.true = newlabel();\ B_1.false = B.false;\ \}\ B_1$
$\quad \&\& \{\ label(B_1.true);\ B_2.true = B.true;\ B_2.false = B.false;\ \}\ B_2$

$S \rightarrow if \{\ B.true = newlabel();$
$\quad B.false = S.next;\ \}$
$\quad (\ B\ ) \{\ label(B.true);\ S_1.next = S.next;\ \}$
$\quad S_1$

Handle Non-L-Attribute Labels[处理非左]

- Idea: generate code using dummy labels first, then patch them with addresses later after labels are generated
- **Two-pass** approach: requires two scans of code
  - Pass 1:
    - Generate code creating dummy labels for forward jumps. (Insert label into a hashtable when created)
    - When label emitted, record address in hashtable
  - Pass 2:
    - Replace dummy labels with target addresses (Use previously built hashtable for mapping)
- **One-pass** approach
  - Generate holes when forward jumping to a un-generated label
  - Maintain a list of holes for that label
  - Fill in holes with addresses when label generated later on

Two-Pass Code Generation[两遍生成]

- **newlabel()**: generates a new dummy label
  - Label inserted into hashtable, initially with no address
- Pass 1: generate code with non-address-mapped labels
  - For *S -> if (B) S_1*:
    - Dummy labels: *B.true=newlabel(); B.false=S.next;*
    - Generate *B.code* using dummy labels *B.true*, *B.false*
    - Generate label *B.true*: in the process mapping it to an address
    - Generate *S_1.code* using dummy label *S_1.next*
- Pass 2: Replace labels with addresses using hashtable
  - Any forward jumps to dummy labels *B.true*, *B.false* are replaced with jump target addresses

```
   IfFalse B goto S.next
B.true:
   S1.code
S.next:
```

```
S -> if { B.true = newlabel();
          B.false = S.next; }
     ( B ) { label(B.true); S1.next = S.next; }
     S1
```

One-Pass Code Generation[单遍生成]

- If *L-attributed*, grammar can be processed in one pass
- However, forward jumps introduce *non-L-attributes*
  - E.g. $E_1.false = E_2.label$ in $E \rightarrow E_1 \mid\mid E_2$
  - We need to know address of $E_2.label$ to insert jumps in E_1
  - Is there a general solution to this problem?
- Solution: **Backpatching**[回填]
  - Leave holes in IR in place of forward jump addresses
  - Record indices of jump instructions in a hole list
  - When target address of label for jump is eventually known, backpatch holes using the hole list for that particular label
- Can be used to handle any *non-L-attribute* in a grammar

Backpatching[回填]

- Synthesized attributes[综合属性]. *S -> if (B) S_1*
  - *B.truelist*: a list of jump or conditional jump insts into which we must insert the label to which control goes if *B* is true[B为真时控制流应该转向的指令的标号]
  - *B.falselist*: a list of insts that eventually get the label to which control goes when *B* is false[B为假时控制流应该转向的指令的标号]
  - *S.nextlist*: a list of jumps to the inst immediately following the code for *S*[紧跟在S代码之后的指令的标号]
- Functions to implement backpatching
  - *makelist(i)*: creates a new list out of statement index *i*
  - *merge(p_1, p_2)*: returns merged list of p_1 and p_2
  - *backpatch(p, i)*: fill holes in list *p* with statement index *i*

Backpatching (cont.)

- $B \rightarrow B_1 \,||\, M\ B_2$
  - If B_1 is true, then B is also true
  - If B_1 is false, we must next test B_2, so the target for jump $B_1.falselist$ must be the beginning of the code of B_2

① $B \rightarrow E_1\ relop\ E_2$ { *B.truelist = makelist(nextinst);*
B.falselist = makelist(nextinst+1);
gen('if' E_1.addr relop E_2.addr 'goto _');
gen('goto _'); }

② $B \rightarrow B_1 \,||\, M\ B_2$ { *backpatch(B_1.falselist, M.inst);*
B.truelist = merge(B_1.truelist, B_2.truelist);
B.falselist = B_2.falselist; }

③ $B \rightarrow B_1$ && $M\ B_2$ { *backpatch(B_1.truelist, M.inst);*
B.truelist = B_2.truelist;
B.falselist = merge(B_1.falselist, B_2.falselist); }

④ $M \rightarrow \varepsilon$ { *M.inst = nextinst; }*

M: causes a semantic action to pick up the index of the next inst to be generated.

Example

① $B \rightarrow E_1$ relop E_2 { B.truelist = makelist(nextinst);
B.falselist = makelist(nextinst+1);
gen('if' E_1.addr relop E_2.addr 'goto _');
gen('goto _'); }

② $B \rightarrow B_1$ || M B_2 { backpatch(B_1.falselist, M.inst);
B.truelist = merge(B_1.truelist, B_2.truelist);
B.falselist = B_2.falselist; }

③ $B \rightarrow B_1$ && M B_2 { backpatch(B_1.truelist, M.inst);
B.truelist = B_2.truelist;
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Arbitarily start inst numbers at 100

a < b || c < d && e < f

Example

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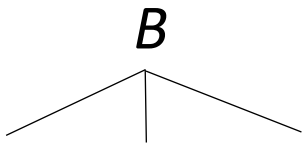

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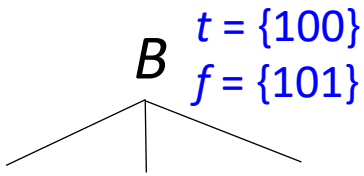

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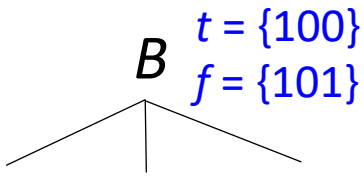
100: if a < b: goto _

101: goto _



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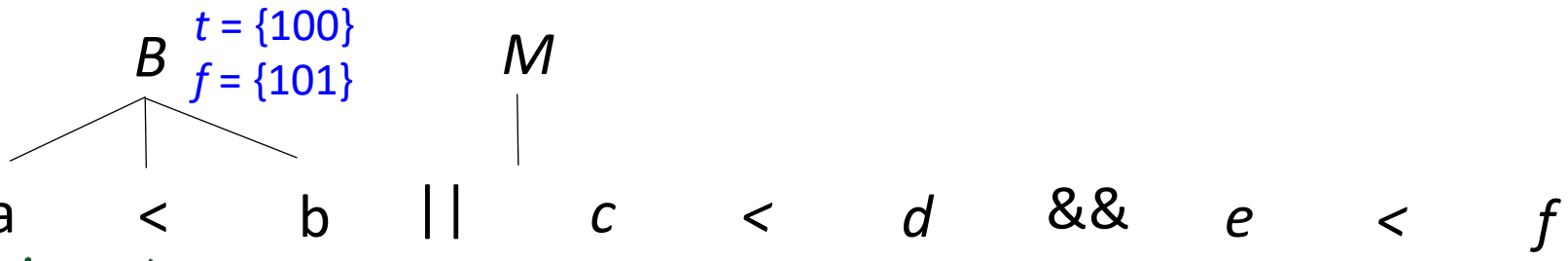

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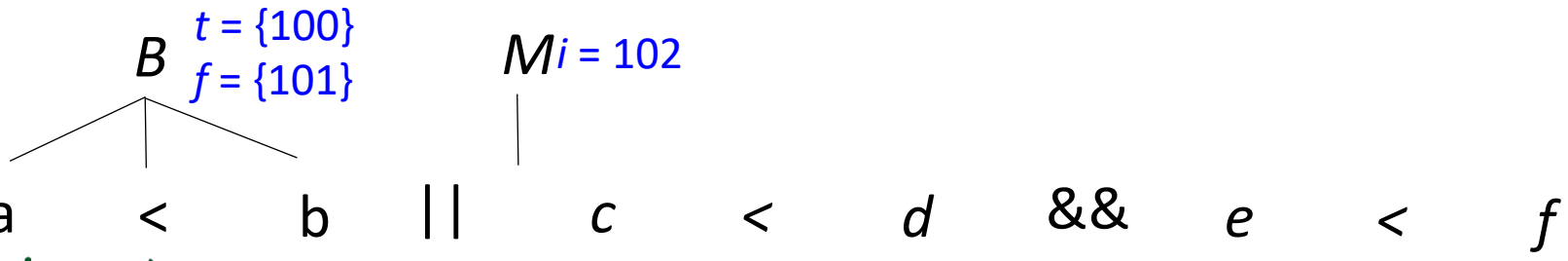

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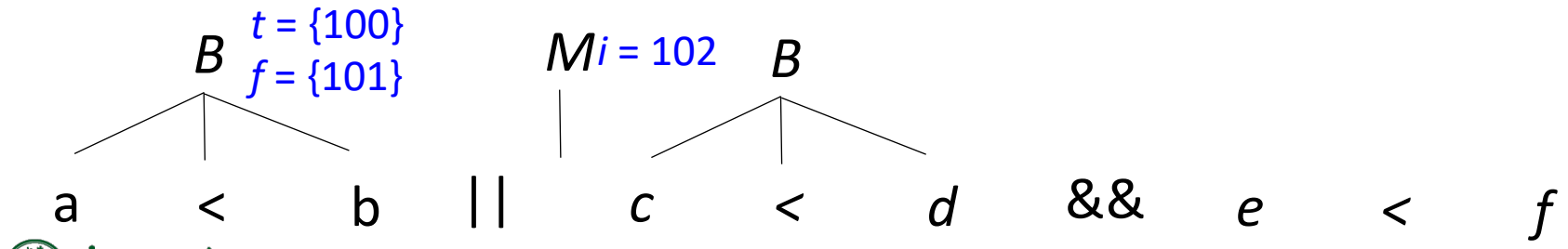

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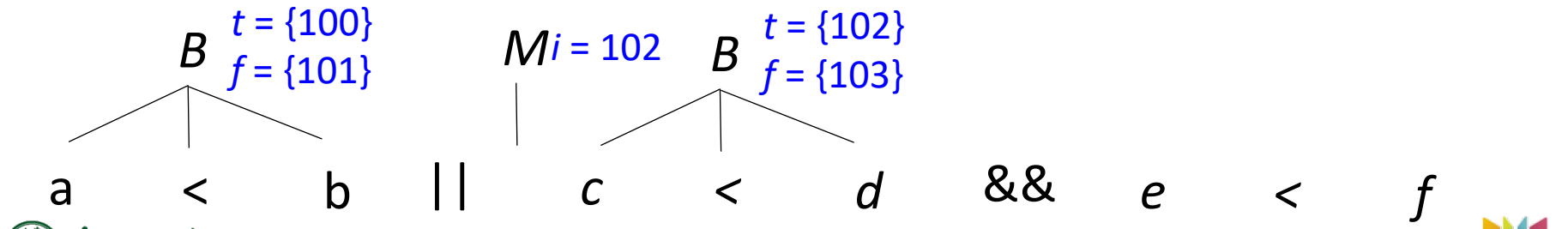

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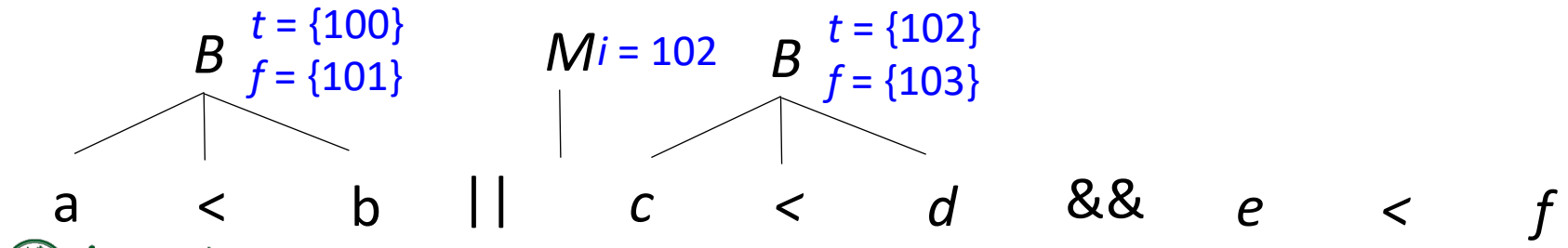
Arbitarily start inst numbers at 100

100: if a < b: goto _

101: goto _

102: if c < d: goto _

103: goto _



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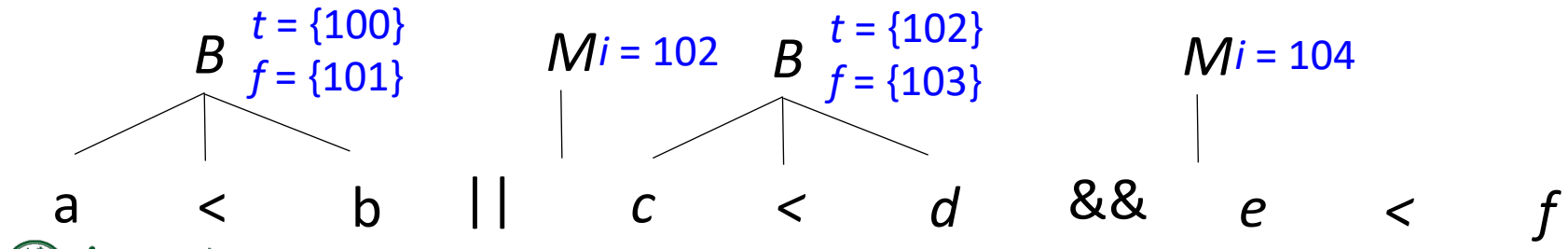

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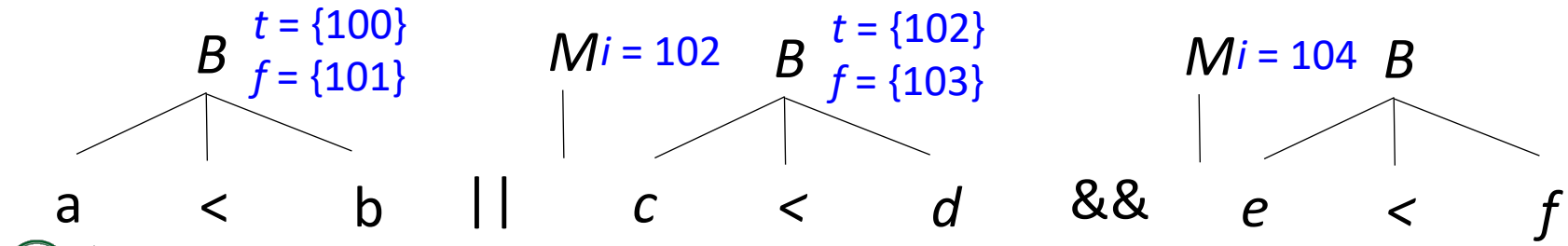
B.falselist = merge(B_1.falselist, B_2.falselist); }

④ $M \rightarrow \varepsilon$ { M.inst = nextinst; }

Arbitarily start inst numbers at 100

100: if a < b: goto _
101: goto _
102: if c < d: goto _
103: goto _



Example

① $B \rightarrow E_1$ relop E_2 { B.truelist = makelist(nextinst);
B.falselist = makelist(nextinst+1);
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gen('goto _'); }

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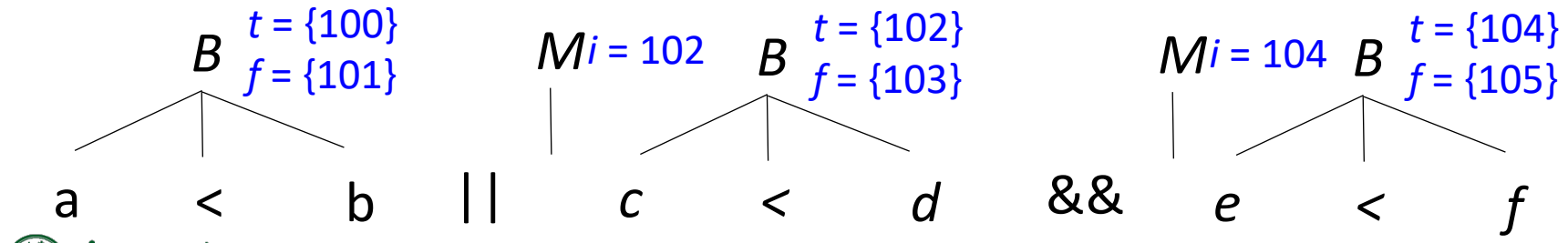

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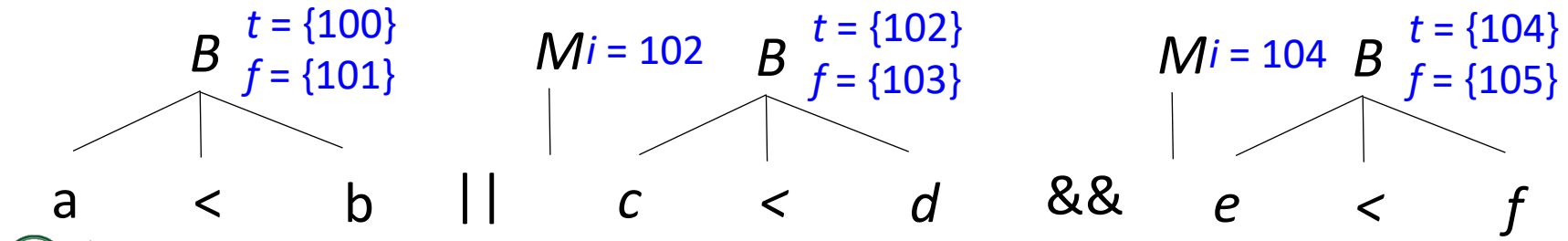

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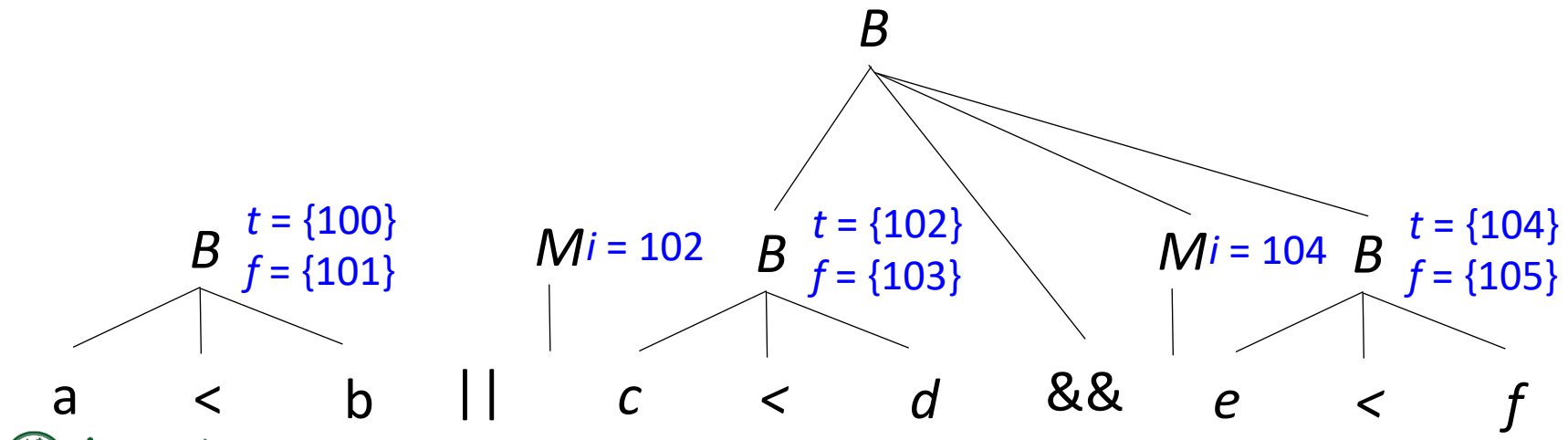

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104: if e < f: goto _
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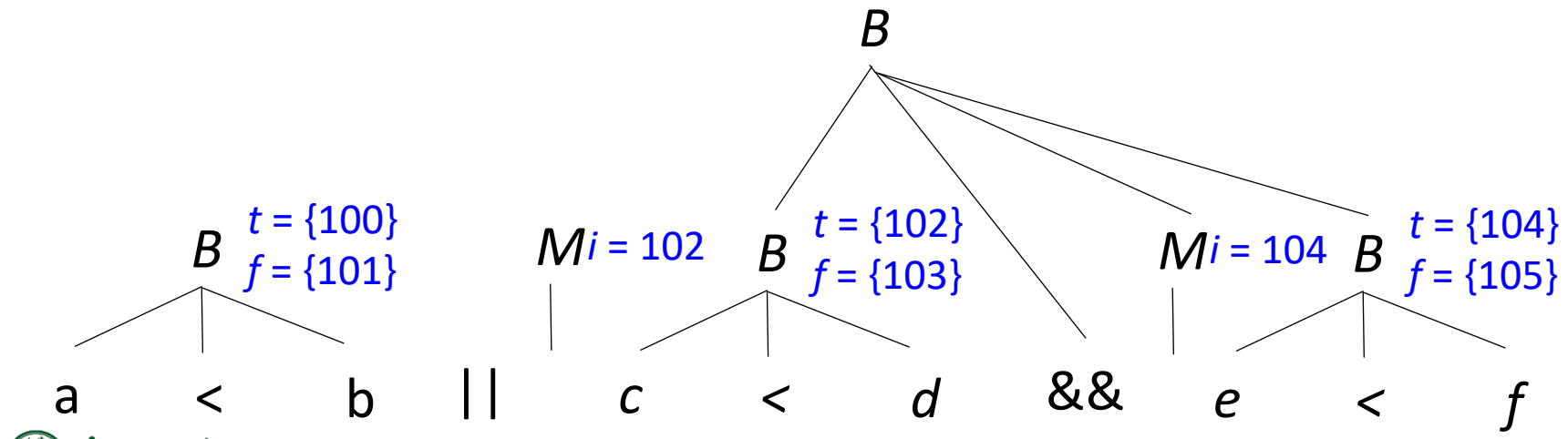
backpatch(B_1.truelist, M.inst) → backpatch(102, 104)



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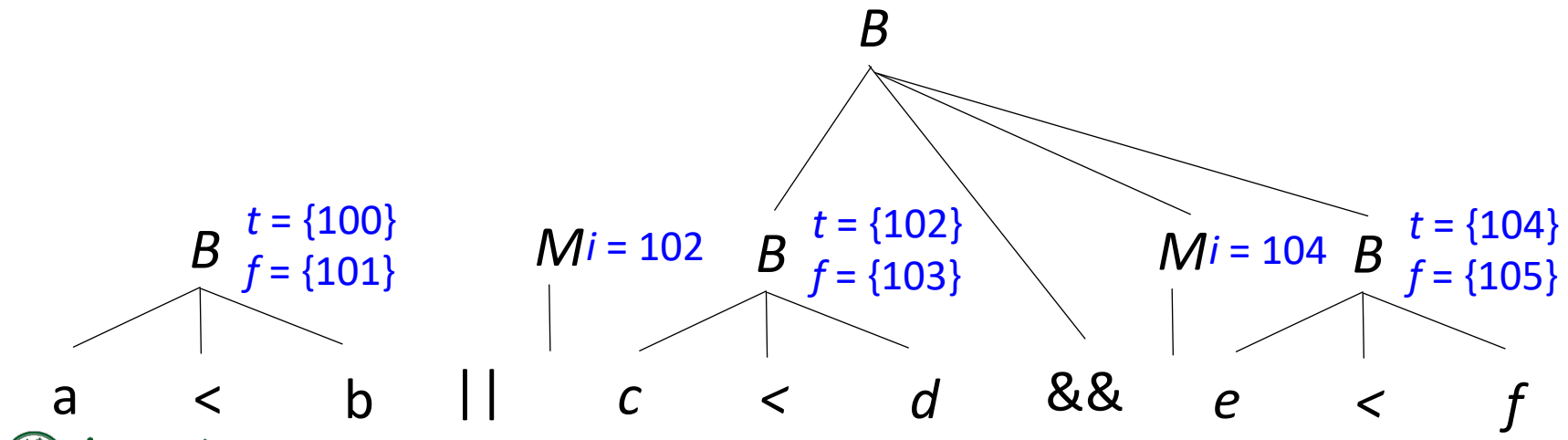

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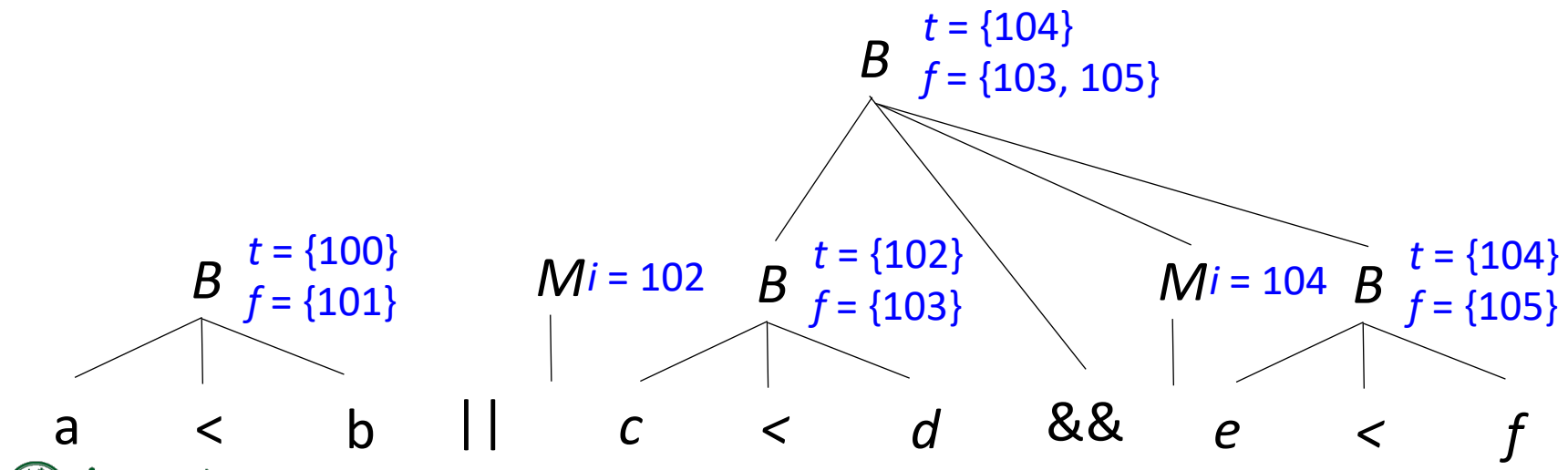

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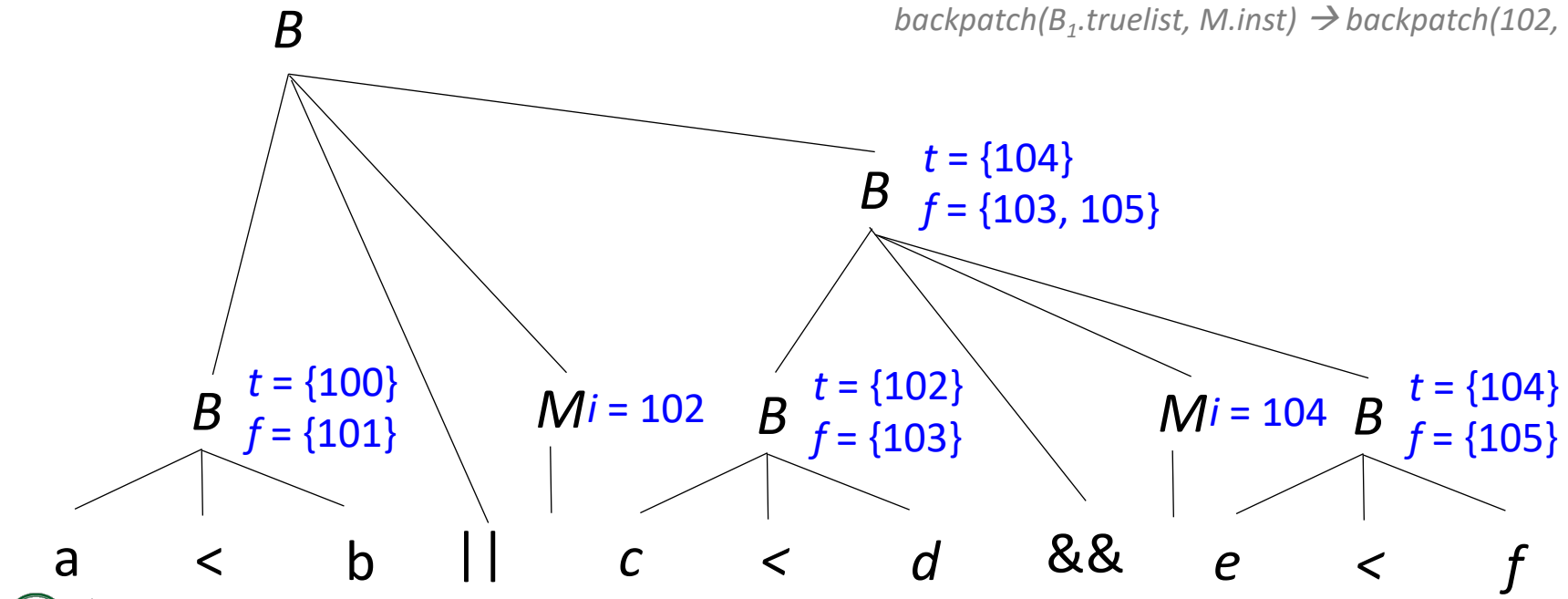

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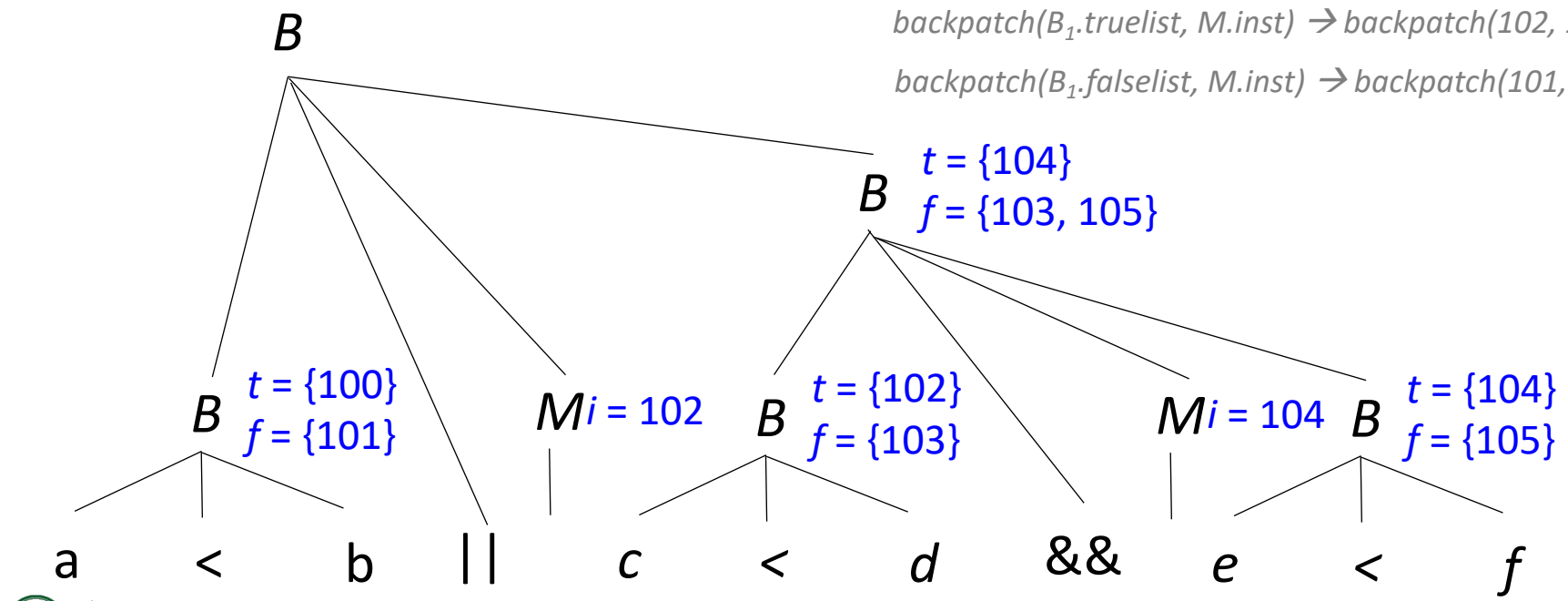

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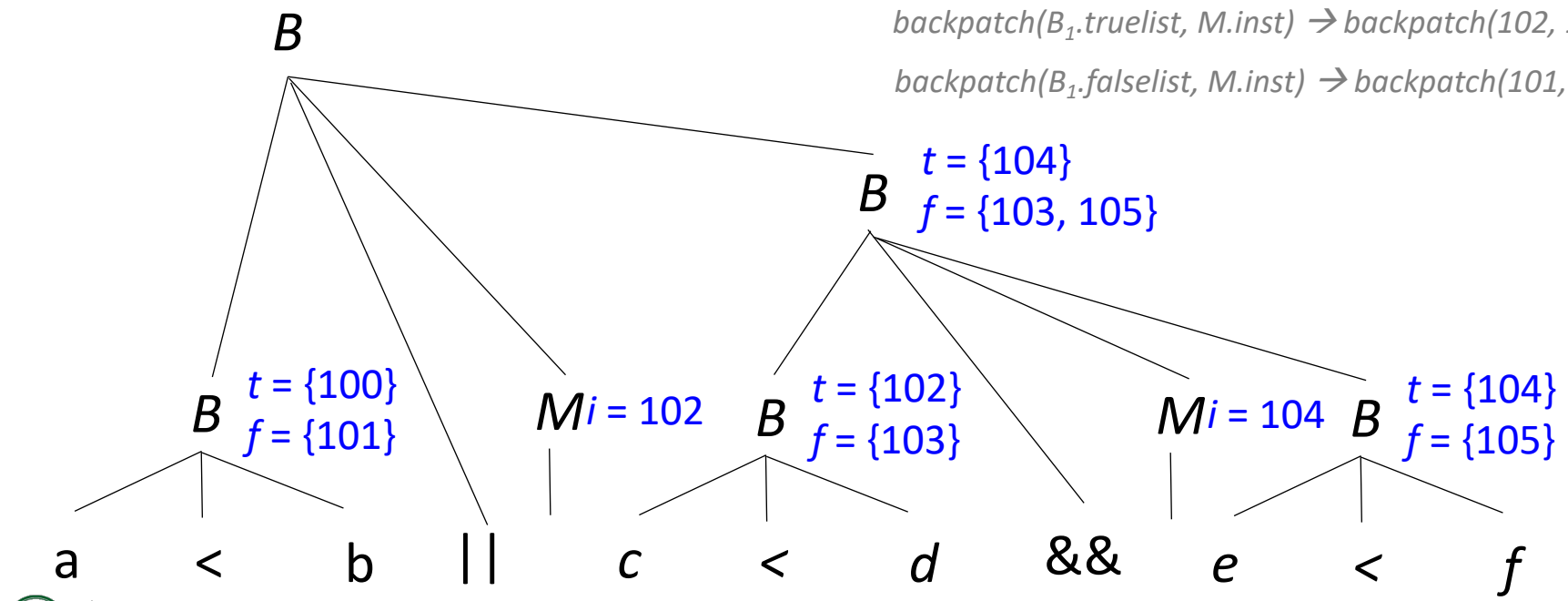

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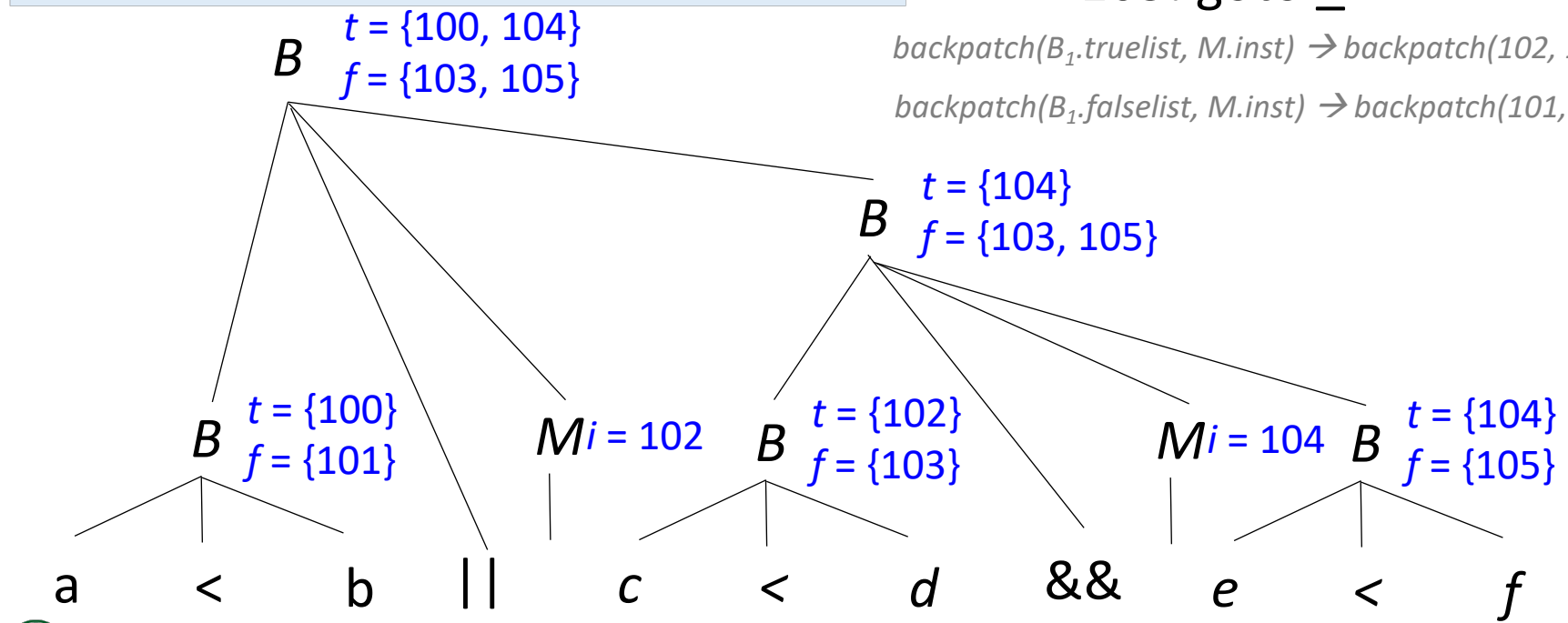


Backpatching of Control-Flow

- *S.nextlist*: a list of all jumps to the inst following *S*

① $S \rightarrow \text{if } (B)\ M\ S_1$ { backpatch(B.truelist, M.inst)
S.nextlist = merge(B.falselist, S_1.nextlist); }

② $S \rightarrow \text{if } (B)\ M_1\ S_1\ N\ \text{else}\ M_2\ S_2$ { backpatch(B.truelist, M_1.inst);
backpatch(B.falselist, M_2.inst);
temp = merge(S_1.nextlist, N.nextlist);
S.nextlist = merge(temp, S_2.nextlist); }

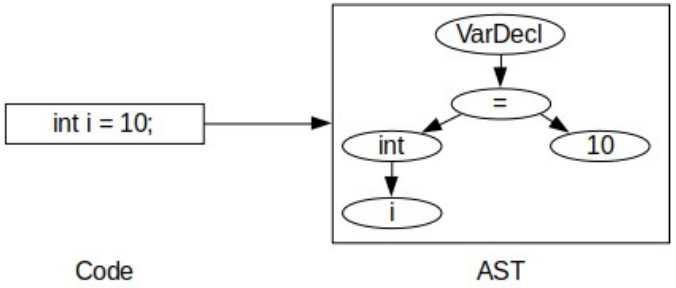
③ $S \rightarrow \text{while } M_1\ (B)\ M_2\ S_1$ { backpatch(S_1.nextlist, M_1.inst);
backpatch(B.truelist, M_2.inst);
S.nextlist = B.falselist);
gen('goto' M_1.inst); }

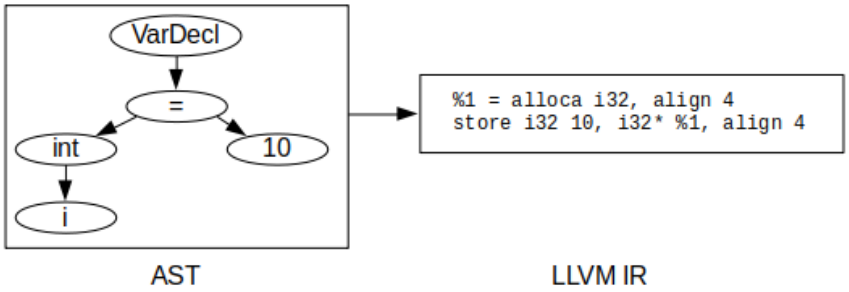
④ $M \rightarrow \varepsilon$ { M.inst = nextinst; }

⑤ $N \rightarrow \varepsilon$ { N.nextlist = makelist(nextinst);
gen('goto _'); }

Summary

- Code generation: generate TAC instructions using separate AST traversal (LLVM) or syntax directed translation
  - Variable definitions[变量定义]
  - Expressions and statements
    - Assignment[赋值]
    - Array references[数组引用]
    - Boolean expressions[布尔表达式]
    - Control-flow[控制流]
- Translations not covered
  - Switch statements[switch语句]
  - Procedure calls[过程调用]





LLVM

```c
int main() {
  int a, b, c;
  a = b + c;
  a = 3;

  if (a > 0) return 1;
  else return 0;
}
```

clang -emit-llvm -S -O0 xx.c

```
define dso_local i32 @main() #0 {
  %1 = alloca i32, align 4
  %2 = alloca i32, align 4
  %3 = alloca i32, align 4
  %4 = alloca i32, align 4
  store i32 0, i32* %1, align 4
  %5 = load i32, i32* %3, align 4
  %6 = load i32, i32* %4, align 4
  %7 = add nsw i32 %5, %6
  store i32 %7, i32* %2, align 4
  store i32 3, i32* %2, align 4
  %8 = load i32, i32* %2, align 4
  %9 = icmp sgt i32 %8, 0
  br i1 %9, label %10, label %11

10:
  store i32 1, i32* %1, align 4
  br label %12

11:
  store i32 0, i32* %1, align 4
  br label %12

12:
  %13 = load i32, i32* %1, align 4
  ret i32 %13
}
```

clang -emit-llvm -S -O1 xx.c

```
define dso_local i32 @main() local_unnamed_addr #0 {
  ret i32 1
}
```

中山大學 SUN YAT-SEN UNIVERSITY
计算机学院（软件学院）
SCHOOL OF COMPUTER SCIENCE AND ENGINEERING

Compilation Principle
编 译 原 理

第20讲：代码优化(1)

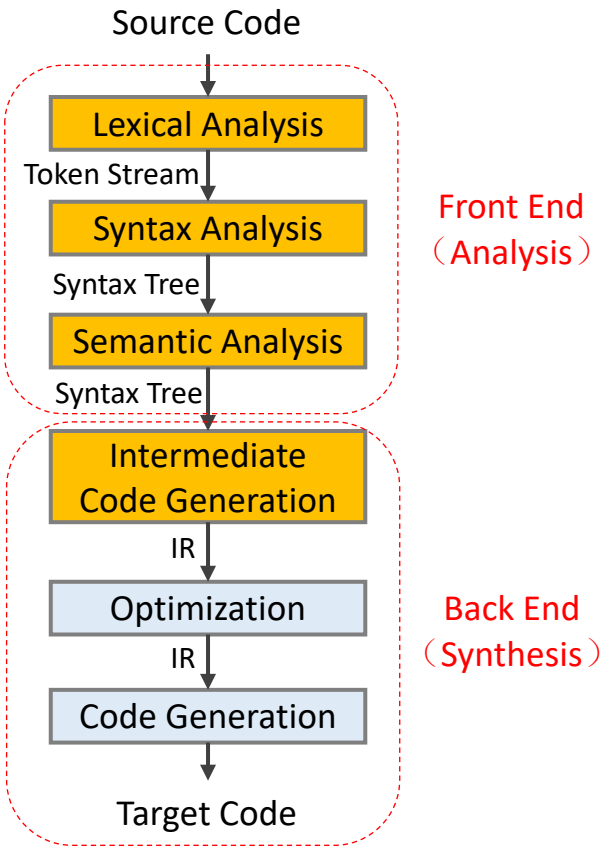
张献伟

xianweiz.github.io

DCS290, 5/23/2024

Optimization[代码优化]

- What we have now
  - IR of the source program (+symbol table)
- Goal of optimization[优化目标]
  - Improve the IR generated by the previous step to take better advantage of resources
- A very active area of research[研究热点]
  - Front end phases are well understood
  - Unoptimized code generation is relatively straightforward
  - Many optimizations are NP-complete
    - Thus usually rely on heuristics and approximations



To Optimize: Who, When, Where?

- Manual: source code[人工, 源码]
  - Select appropriate algorithms and data structures
  - Write code that the compiler can effectively optimize
    - Need to understand the capabilities and limitations of compiler opts
- **Compiler**: intermediate representation[编译器, IR]
  - To generate more efficient TAC instructions
- **Compiler**: final code generation[编译器, 目标代码]
  - E.g., selecting effective instructions to emit, allocating registers in a better way
- Assembler/Linker: after final code gen[汇编/链接, 目标代码]
  - Attempting to re-work the assembly code itself into something more efficient (e.g., link-time optimization)

Example

```
int find_min(const int* array, const int len) {
   int min = a[0];
   for (int i = 1; i < len; i++) {
      if (a[i] < min) { min = a[i]; }
   }
   return min;
}
int find_max(const int* array, const int len) {
    int max = a[0];
    for (int i = 1; i < len; i++) {
       if (a[i] > max) { max = a[i]; }
    }
    return min;
}
void main() {
   int* array, len, min, max;
   initialize_array(array, &len);
   min = find_min(array, len);
   max = find_max(array, len);
   ...
}
```

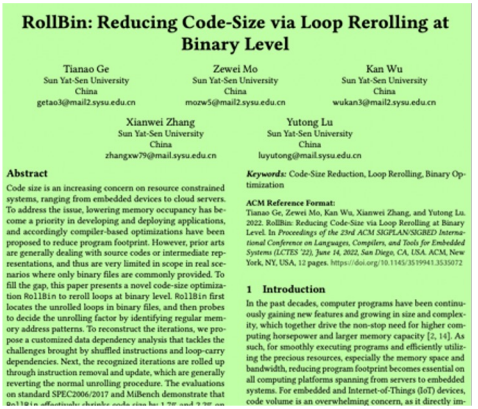
Inline

Loop merge

```
void main() {
    int* array, len, min, max;
    initialize_array(array, &len);
    min = a[0]; max = a[0];
    for (int i = 0; i < len; i++) {
       if (a[i] < min) { min = a[i]; }
       if (a[i] > max) { max = a[i]; }
    }
    ...
}
```

Overview of Optimizations

- Goal of optimization is to generate **better** code[更好的代码]
  - Impossible to generate **optimal** code (so, it is improvement, actually)
    - Factors beyond control of compiler (user input, OS/HW design) all affect what is optimal
    - Even discounting above, it's still a NP-complete problem
- Better one or more of the following (in the average case)
  - **Execution time**[运行时间]
  - **Memory usage**[内存使用]
  - Energy consumption[能耗]
    - To reduce energy bill in a data center
    - To improve the lifetime of battery powered devices
  - Binary executable size[可执行文件大小]
    - If binary needs to be sent over the network
    - If binary must fit inside small device with limited storage
  - Other criteria[其他]
- Should never change program semantics[正确性是前提]



RollBin: Reducing Code-Size via Loop Rerolling at Binary Level

Tianao Ge, Sun Yat-Sen University, China, getao3@mail2.sysu.edu.cn
Zewei Mo, Sun Yat-Sen University, China, mozw5@mail2.sysu.edu.cn
Kan Wu, Sun Yat-Sen University, China, wukan3@mail2.sysu.edu.cn
Xianwei Zhang, Sun Yat-Sen University, China, zhangxw79@mail.sysu.edu.cn
Yutong Lu, Sun Yat-Sen University, China, luyutong@mail.sysu.edu.cn

Abstract

Code size is an increasing concern on resource constrained systems, ranging from embedded devices to cloud servers. To address the issue, lowering memory occupancy has become a priority in developing and deploying applications, and accordingly compiler-based optimizations have been proposed to reduce program footprint. However, prior arts are generally dealing with source codes or intermediate representations, and thus are very limited in scope in real scenarios where only binary files are commonly provided. To fill the gap, this paper presents a novel code-size optimization RollBin to reroll loops at binary level. RollBin first locates the unrolled loops in binary files, and then probes to decide the unrolling factor by identifying regular memory address patterns. To reconstruct the iterations, we propose a customized data dependency analysis that tackles the challenges brought by shuffled instructions and loop-carry dependencies. Next, the recognized iterations are rolled up through instruction removal and update, which are generally reverting the normal unrolling procedure. The evaluations on standard SPEC2006/2017 and MiBench demonstrate that

Keywords: Code-Size Reduction, Loop Rerolling, Binary Optimization

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1 Introduction

In the past decades, computer programs have been continuously gaining new features and growing in size and complexity, which together drive the non-stop need for higher computing horsepower and larger memory capacity [2, 14]. As such, for smoothly executing programs and efficiently utilizing the precious resources, especially the memory space and bandwidth, reducing program footprint becomes essential on all computing platforms spanning from servers to embedded systems. For embedded and Internet-of-Things (IoT) devices, code volume is an overwhelming concern, as it directly im-

Types of Optimizations[分类]

- Compiler optimization is essentially a transformation[转换]
  - Delete / Add / Move / Modify something
- **Layout-related** transformations[布局相关]
  - Optimizes *where* in memory code and data is placed
  - Goal: maximize **spatial locality**[空间局部性]
    - Spatial locality: on an access, likelihood that nearby locations will also be accessed soon
    - Increases likelihood subsequent accesses will be faster
      - E.g. If access fetches cache line, later access can reuse
      - E.g. If access page faults, later access can reuse page
- **Code-related** transformations[代码相关]
  - Optimizes *what* code is generated
  - Goal: execute least number of most costly instructions

Layout-Related Opt.: Code

- Two ways to layout code for the below example

```
f() {
  …
  h();
  …
}
g() {
  …
}
h() {
  …
}
```

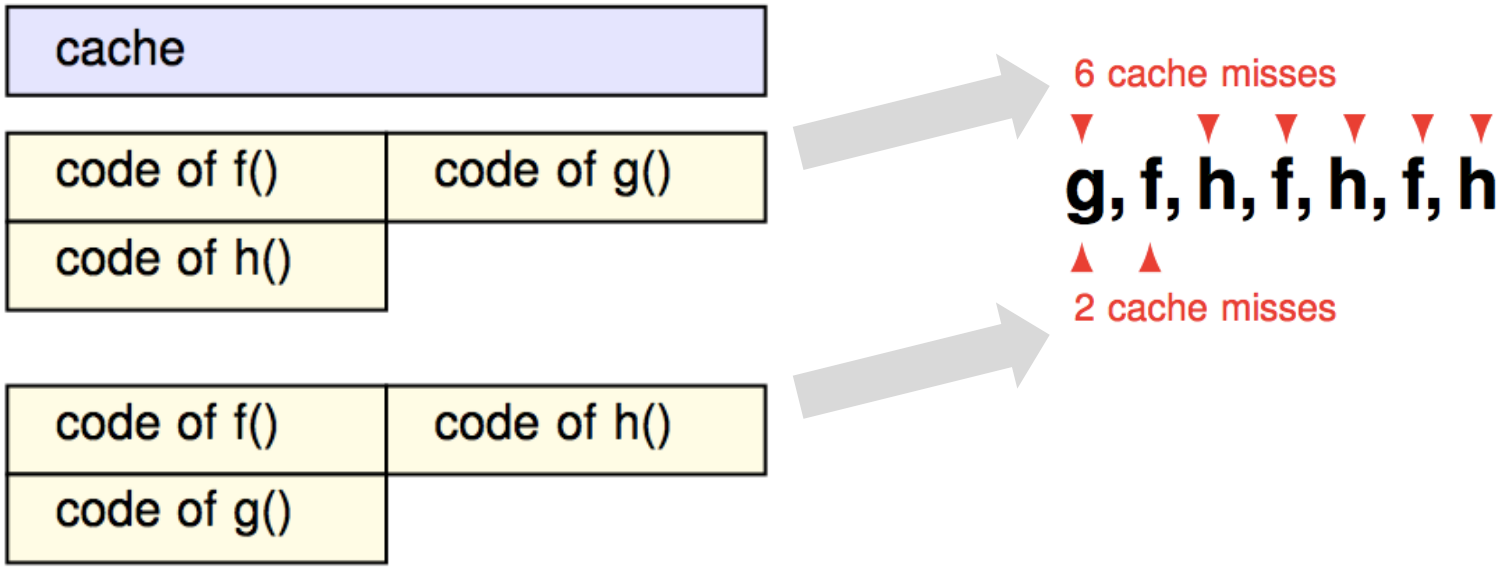
code of f()
code of g()
code of h()

OR

code of f()
code of h()
code of g()

Layout-Related Opt.: Code (cont.)

- Which code layout is better?
- Assume
  - data cache has one *N*-word line
  - the size of each function is *N/2*-word long
  - access sequence is "**g, f, h, f, h, f, h**"



Layout-Related Opt.: Data

- Change the variable declaration order

```
struct S {
  int x1;
  int x2[200];
  int x3;
} obj[100];

for(...) {
  ... = obj[i].x1 + obj[i].x3;
}
```

```
struct S {
  int x1;
  int x3;
  int x2[200];
} obj[100];

for(...) {
  ... = obj[i].x1 + obj[i].x3;
}
```

- Improved spatial locality
  - Now x1 and x3 likely reside in same cache line
  - Access to x3 will always hit in the cache

Layout-Related Opt.: Data (cont.)

- Change AOS (array of structs) to SOA (struct of arrays)

```
struct S {
   int x;
   int y;
} points[100];

for(...) {
   ... = points[i].x * 2;
}
for(...) {
   ... = points[i].y * 2;
}
```

```
struct S {
   int x[100];
   int y[100];
} points;

for(...) {
   ... = points.x[i] * 2;
}
for(...) {
   ... = points.y[i] * 2;
}
```

- Improved spatial locality for accesses to ‘x’s and ‘y’s

Structure Peeling[结构分离]

```
struct S {
  int A;
  int B;
  int C;
};
```

A,C – Hot fields
B – Cold field

Peeled structures:

```
struct S.Hot {
  int A;
  int C;
};
```

```
struct S.Cold {
  int B;
};
```

https://llvm.org/devmtg/2014-10/Slides/Prashanth-DLO.pdf
https://llvm.org/devmtg/2021-02-28/slides/Prashantha-MLIR-LTO.pdf

Code-Related Optimizations

- Modifying code — e.g. **strength reduction**[强度削减]

  A=2*a; ≡ A=a«1;

- Deleting code — e.g. **dead code elimination**

  A=2; A=y; ≡ A=y;

- Moving code — e.g. **code scheduling**

  A=x*y; B=A+1; C=y; ≡ A=x*y; C=y; B=A+1;

  (Now C=y; can execute while waiting for A=x*y;)

- Inserting code — e.g. **data prefetching**[数据预取]

  while (p!=NULL)

  { process(p); p=p->next; }

  ≡

  while (p!=NULL)

  { prefetch(p->next); process(p); p=p->next; }

  (Now access to p->next is likely to hit in cache)